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Attn: Examiner PATEL
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Facsimile Center
Washington, D.C. 20231

FROM: Douglas P. Mueller
OUR REF: 10873.644US01
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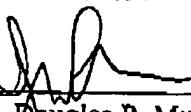
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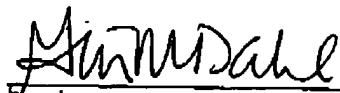
Amendment and Response to Election of Species Requirement (originally filed March 19, 2004)

Applicant: KUSUMOTO
Serial No.: 09/773,502
Filed: 02/02/01
Group Art Unit: 2829
Confirmation No.
Our Ref. No.: 10873.644US01

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By: 
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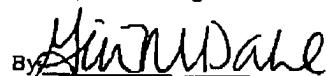
January 19, 2005
Date

S/N 09/773,502

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	KUSUMOTO	Examiner:	PATEL, P.
Serial No.:	09/773,502	Group Art Unit:	2829
Filed:	February 2, 2001	Docket No.:	10873.644US01
Title:	SEMICONDUCTOR DEVICE HAVING A CONNECTION INSPECTING CIRCUIT FOR INSPECTING CONNECTION OF POWER SOURCE TERMINALS AND GROUNDING TERMINALS, AND INSPECTION METHOD FOR THE SAME		

CERTIFICATE UNDER 37 CFR 1.6(d): I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on January 11, 2005.


By: _____
Name: Gina M. Dahl

AMENDMENT AND RESPONSE TO
ELECTION OF SPECIES REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants elect Species V (Fig. 11) in response to the Election of Species Requirement of March 1, 2004. Claims 68, 70, 73 and 74 read upon the elected species. Please amend the claims as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 4 of this paper.

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Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

Claims 1-66 are canceled.

67. (Withdrawn) A semiconductor device having a semiconductor integrated circuit, the semiconductor device comprising:

a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;

wherein all of the first plurality of internal terminals are connected to the first internal power source line via a first switching portion.

68. (Currently amended) The A semiconductor device according to claim 67, further comprising another internal terminal that is connected directly to the first internal power source line not via the first switching portion, having a semiconductor integrated circuit, the semiconductor device comprising:

a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;

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wherein one of the first plurality of internal terminals is connected directly to the first internal power source line, and others thereof are connected to the first internal power source line via a first switching portion.

69. (Withdrawn) The semiconductor device according to claim 67, comprising:
a second plurality of leads for supplying the semiconductor device with a second power source from the outside;
a second internal power source line for supplying the internal circuit with the second power source; and
a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;
wherein all of the second plurality of internal terminals are connected to the second internal power source line via a second switching portion.

70. (Currently amended) The semiconductor device according to claim 69 68, further comprising another internal terminal that is connected directly to the second internal power source line, not via a switching portion;
a second plurality of leads for supplying the semiconductor device with a second power source from an outside;
a second internal power source line for supplying the internal circuit of the semiconductor integrated circuit with the second power source; and
a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;
wherein one of the second plurality of internal terminals is connected directly to the second internal power source line, and others thereof are connected to the second internal power source line via a second switching portion.

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71. (Withdrawn) The semiconductor device according to claim 69, further comprising a third switching portion provided between the first and second internal power source lines.
72. (Withdrawn) The semiconductor device according to claim 69, further comprising a current detecting circuit provided between the first and second internal power source lines.
73. (Currently amended) The semiconductor device according to claim 67 68, wherein the internal circuit is a group of digital circuits.
74. (Currently amended) The semiconductor device according to claim 67 68, wherein the first switching portion comprises a plurality of switching elements, and each of the first plurality of internal terminals is connected to the first internal power source line via a different switching element of the plurality of switching elements.

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An early and favorable action on the merits is requested. Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

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Dated: January 19, 2005

DPM/gnd